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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10082599	FILING DATE 02/22/2002	CLASS 156 438	SUBCLASS 345.31	GAU 1763 2842	EXAMINER Mills-Moore
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**\*\*CONTINUING DATA VERIFIED:**

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**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		MI22-1941
Verified and Acknowledged Examiners's initials		
TITLE : Interfacial structure for semiconductor substrate processing chambers and substrate transfer chambers and for semiconductor substrate processing chambers and accessory attachments, <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>		

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
Assistant Examiner		Total Claims	Print Claim for I.O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drawn	Figs. Drawn
Primary Examiner		Print Fig.	
<input type="checkbox"/> <b>TERMINAL</b>		Application Examiner	
<b>DISCLAIMER</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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